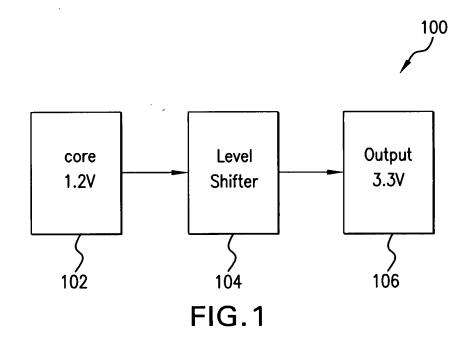
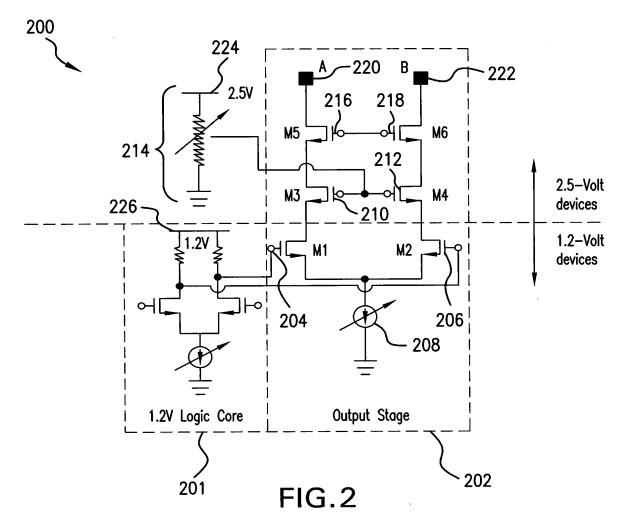


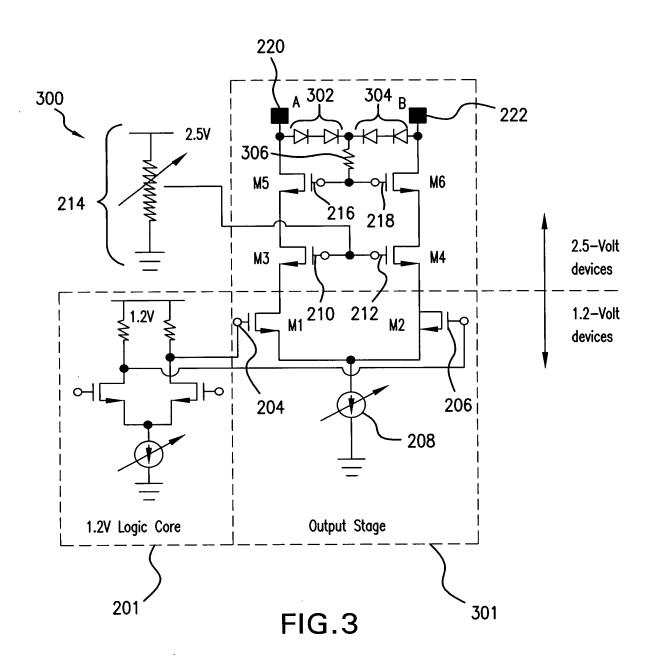
Inventors: JIANG et al. Appl. No. 10/751,508

Sheet 1 of 5





Inventors: JIANG *et al.* Appl. No. 10/751,508 Sheet 2 of 5



Inventors: JIANG *et al.* Appl. No. 10/751,508 Sheet 3 of 5

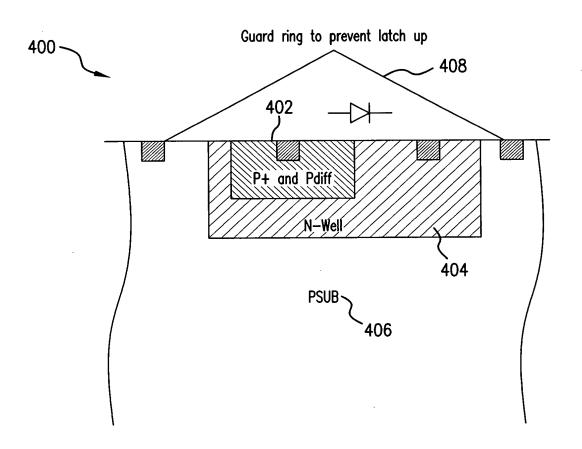


FIG.4

Inventors: JIANG et al. Appl. No. 10/751,508 Sheet 4 of 5

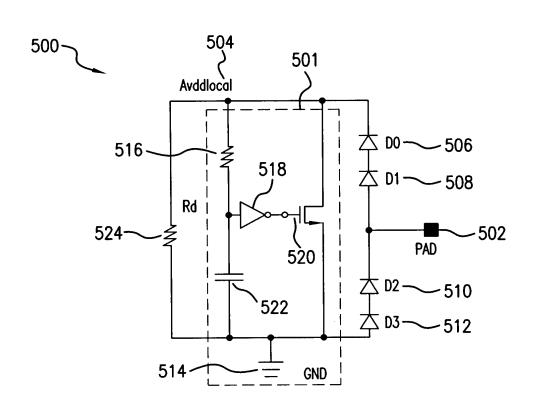


FIG.5

Inventors: JIANG et al. Appl. No. 10/751,508 Sheet 5 of 5 REPLACEMENT SHEET

600

Avddlocal

≱ Rd

604

601 3.3V 50 50 From other chip Avddlocal **本 D0** 本 D1 D0 本 502 602 ¦ D1 本 **≻606** Rd≹ PAD

 → D2

 → D3

PAD D2 本 D3 本 GND ± GND В 222 220 2.50 R0

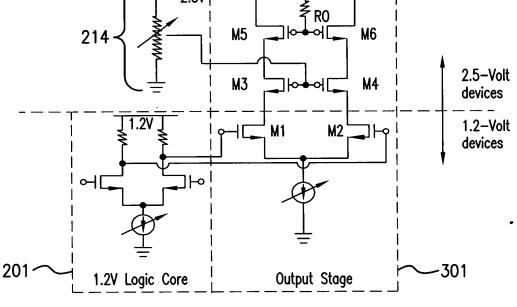


FIG.6